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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Koji YAMAGUCHI

Group Art Unit: 2822

Application No.: 10/753,357

Examiner:

Filed: January 9, 2004

Docket No.: 118292

For: WIRING SUBSTRATE, SEMICONDUCTOR DEVICE, SEMICONDUCTOR MODULE, ELECTRONIC EQUIPMENT, METHOD FOR DESIGNING WIRING SUBSTRATE, METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE, AND METHOD FOR MANUFACTURING .

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

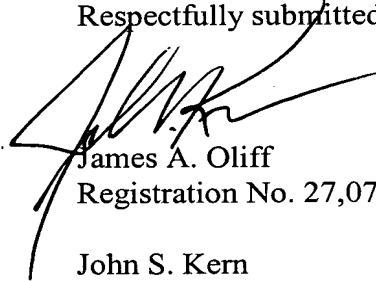
In reply to the June 8, 2005 Restriction Requirement, Applicant provisionally elects Group I, claims 1-14, drawn to a wiring substrate, a semiconductor device and an electronic device, classified in class 257, subclass 774, with traverse.

It is also respectfully submitted that the subject matter of all claims 1-22 is sufficiently related that a thorough search for the subject matter of any one Group of claims would encompass a search for the subject matter of the remaining claims. Thus, it is respectfully submitted that the search and examination of the entire application could be made without serious burden. See MPEP §803 in which it is stated that "if the search and examination of an entire application can be made without serious burden, the examiner must examine it on the merits, even though it includes claims to independent or distinct inventions" (emphasis added). It is respectfully submitted that this policy should apply in the present application in

order to avoid unnecessary delay and expense to Applicant and duplicative examination by the Patent Office.

Thus, withdrawal of the Restriction Requirement is respectfully requested.

Respectfully submitted,



James A. Oliff  
Registration No. 27,075

John S. Kern  
Registration No. 42,719

JAO:JSK/kap

Date: July 8, 2005

**OLIFF & BERRIDGE, PLC**  
**P.O. Box 19928**  
**Alexandria, Virginia 22320**  
**Telephone: (703) 836-6400**

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